

09/583502

Jyoti Das

## EAST SEARCH

7/21/03

## Databases

L#	Hits	Search String	Databases
L1	29584	computer aided design or CAD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L7	0	4 and ((genrat\$3 or updat\$3) with relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L8	90	1 and ((genrat\$3 or updat\$3) with relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L10	87	1 and ("parent child relationship") or ("ancestor descendant relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L11	691	1 and (size or mate or update or positional) with relationship	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L14	0	1 and (genrat\$3 with ("component list" or "reference list"))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L15	135	1 and ("component list" or "reference list")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L17	0	1 and ("design intent" with reallionship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L18	76	1 and "design intent"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L2	7	1 and ((restructur\$3 or regroup\$3 or rearrang\$3) with ("design model" or assembly))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L6	3	1 and ("hierarchical data structure") with component\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L5	15	1 and ("hierarchical relationship") with component\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L4	44	1 and ("hierarchical data structure")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L9	2	8 and ("hierarchical relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L22	5	8 and 10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L12	6	11 and ("hierarchical relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L23	16	10 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L13	1	1 and ((genrat\$3 or updat\$3) with "hierarchical relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L16	1	15 and ("hierarchical relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L24	4	15 and 10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L25	2	15 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L26	0	15 and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L19	3	18 and ("hierarchical relationship")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L27	2	18 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L28	3	18 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L29	2	18 and 10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L30	1	18 and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L21	9	1 and "assembly tree"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L31	4	20 and 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L32	10	20 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L33	8	20 and 10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L35	1	20 and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L20	43	1 and ("design tree" or "assembly tree")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L34	0	20 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

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## Results of search set L4: (computer aided design or CAD) and ("hierarchical data structure")

DocumentKind	Codes	Title	Issue Date	Current OR	Abstract
US 20030093767	A1	Method for fabrication of patterns and semiconductor devices	20030515	716/21	
US 20030018948	A1	Method and apparatus for data hierarchy maintenance in a system for mask description	20030123	716/8	
US 20030010937	A1	Pattern generation method and apparatus using cached cells of hierarchical data	20030116	250/492.22	
US 20020178185	A1	Database model, tools and methods for organizing information across external information obj	20021128	715/512	
US 20020107673	A1	Automated connections of computer-aided design components	20020808	703/1	
US 20020089499	A1	Automated three-dimensional alternative position viewer	20020711	345/419	
US 6577992	B1	Transistor level circuit simulator using hierarchical data	20030610	703/14	
US 6574360	B1	Accelerated occlusion culling using directional discretized occluders and system therefore	20030603	382/154	
US 6526406	B1	Database access system to deliver and store information	20030225	707/10	
US 6484300	B1	Systems, methods and computer program products for obtaining an effective pattern density c	20021119	716/7	
US 6470489	B1	Design rule checking system and method	20021022	716/21	
US 6463569	B1	Method for designing a layout of a large scale integrated (LSI) circuit and a recording medium	20021008	716/2	
US 6453452	B1	Method and apparatus for data hierarchy maintenance in a system for mask description	20020917	716/8	
US 6430730	B1	Flash configuration cache	20020806	716/4	
US 6370679	B1	Data hierarchy layout correction and verification method and apparatus	20020409	716/19	
US 6357035	B1	Method and apparatus for the automated generation of programmable interconnect matrices	20020312	716/11	
US 6324680	B1	Synthesis of arrays and records	20011127	716/18	
US 6295627	B1	Method and apparatus for the automated design of memory devices	20010925	716/1	
US 6281881	B1	System and method of adjusting display characteristics of a displayable data file using an erg	20010828	345/164	
US 6148277	A	Apparatus and method for generating model reference tests	20001114	703/22	
US 6120171	A	Fully integrated machinable profile based parametric solid modeler	20000919	700/98	
US 6097371	A	System and method of adjusting display characteristics of a displayable data file using an erg	20000801	345/164	
US 6057849	A	Method of displaying geometric object surfaces	20000502	345/428	
US 6011911	A	Layout overlap detection with selective flattening in computer implemented integrated circuit d	20000104	716/5	
US 6009250	A	Selective flattening in layout areas in computer implemented integrated circuit design	19991228	716/5	
US 5812137	A	Compliant sectioning facility for interactive sectioning of solid geometric objects using a graph	19980922	345/420	
US 5805860	A	Methods, data structures and apparatus for traversing a hierarchical netlist	19980908	716/12	
US 5784059	A	Vehicle navigation system with destination selection using hierarchical menu arrangement wil	19980721	345/854	
US 5761664	A	Hierarchical data model for design automation	19980602	707/100	
US 5712964	A	Computer graphics data display device and method based on a high-speed generation of a ct	19980127	345/418	
US 5657235	A	Continuous scale optical proximity correction by mask maker dose modulation	19970812	700/182	
US 5461712	A	Quadrant-based two-dimensional memory manager	19951024	345/543	
US 5418961	A	Parallel tables for data model with inheritance	19950523	707/1	
US 5272642	A	CAD/CAM device with hierarchical data structure	19931221	700/182	
US 5249265	A	Structure storage management in a graphics display device	19930928	345/853	

US 5182797 A	Multi-processor graphics display system for displaying hierarchical data structures	19930126 345/522
US 5159663 A	Imager and process	19921027 345/422
US 5146404 A	Electronic maintenance support work station	19920908 705/1
US 5068808 A	Imager and process	19911126 345/419
US 4967375 A	Fast architecture for graphics processor	19901030 345/564
JP 06259494 A	STORAGE SYSTEM FOR HIERARCHICAL DATA STRUCTURE	19940916
EP 1122692 A	Computer aided design software package for three-dimensional modelling, involves assessing	20010808
EP 829822 A	Three-dimensional display of surface topography, e.g. for CAD systems - uses four memory re	19980318
DE 4212173 A	CAD-CAM device with hierarchical structure for numerically controlled machines - has input de	19921015